



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Trung T. Doan et al.

Serial No.: 10/774,762

Filed: February 9, 2004

For: APPROACH TO AVOID  
BUCKLING IN BPSG BY  
USING AN INTERMEDIATE  
BARRIER LAYER

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Group Art Unit: 2812

Examiner: Unassigned

Atty Docket: MCRO:144-3  
92-0321-04

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

CERTIFICATE OF TRANSMISSION OR MAILING  
37 C.F.R. 1.8

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June 4, 2004

Date

  
Michael G. Fletcher

Sir:

**INFORMATION DISCLOSURE STATEMENT  
PURSUANT TO 37 C.F.R. §§ 1.97(b)(1) AND 1.98**

In compliance with the duty of disclosure under 37 C.F.R. § 1.56(a), Applicants respectfully request that this Information Disclosure Statement be entered and that the references listed on the PTO-1449 Form be considered by the Examiner and made of record. **In accordance with the revised procedures promulgated by the Office of Patent Legal Administration and relating to applications filed after June 30, 2003, copies of the listed references are not required to be submitted to the Examiner.**

In accordance with 37 C.F.R. § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information

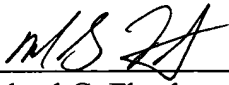
cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b), or as a representation that no other possibly material information, as defined in 37 C.F.R. § 1.56(b), exists.

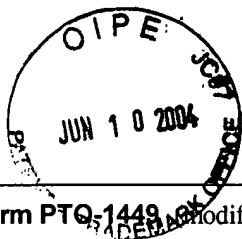
Furthermore, the references listed on the attached PTO-1449 Form are not to be construed as an admission that these references qualify as prior art as to the above-referenced application or any related application. Rather, these references are being presented for the Examiner's consideration without prejudice to Applicants' right to demonstrate that any of these references do not qualify as prior art should the Examiner choose to apply any of these references.

The following information is listed below in accordance with 37 C.F.R. §1.98. Any explanation of non-English language documents contained in this Information Disclosure Statement is believed to constitute a concise explanation of the relevance of the listed reference as it is presently understood by the individual designated in § 1.56(c) most knowledgeable about the content of the listed reference, in accordance with 37 C.F.R. § 1.98(a)(3).

Respectfully submitted,

June 4, 2004

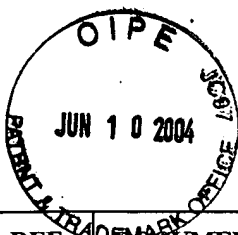
  
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Form PTQ-1448 (Modified)	ATTY. DOCKET NO. MCRO 144-3	SERIAL NO. 10/774,742
List of Patents and Publications For Applicant's Information Disclosure Statement	APPLICANT Trung T. Doan et al	
(Use several sheets if necessary)	FILING DATE 02/09/04	GROUP 2812

#### U.S. PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	3,106,489	10/08/63	Lepselter	117	217	12/09/60
	A2	4,502,209	03/05/85	Eizenberg et al	29	589	08/31/83
	A3	4,784,973	11/15/88	Stevens et al	437	200	08/24/87
	A4	4,814,854	03/21/89	Tigelaar et al	357	67	12/05/86
	A5	4,829,363	05/09/89	Thomas et al	357	71	07/25/88
	A6	4,910,580	03/20/90	Kuecher et al	357	71	08/01/88
	A7	4,937,652	06/26/90	Okumura et al	357	68	09/30/88
	A8	4,994,410	02/19/91	Sun et al	437	192	02/16/90
	A9	5,134,460	07/28/92	Brady et al	357	71	11/02/90
	A10	5,182,420	01/26/93	Steitz et al	174	52.4	04/09/90
	A11	5,317,187	05/31/94	Hindman et al	257	659	02/16/93
	A12	5,372,974	12/31/94	Doan et al	437	240	03/19/93
	A13	5,475,267	12/12/95	Ishi et al	257	752	12/08/94
	A14	4,870,031	09/26/89	Sugahara et al	437	82	09/30/87
	A15	5,284,804	02/08/94	Moslehi	437	228	12/31/91
	A16	6,690,044 B1	02/10/04	Doan et al	257	258	05/20/97
	A17	5,291,058	03/1/94	Samata et al	257	751	07/30/92
	A18	5,155,564	10/13/92	Hishida et al	257	410	11/21/90
	A19	5,412,245	05/02/95	Favreau	257	751	05/23/94
	A20	5,442,226	8/15/95	Maeda et al	257	751	04/26/93

**FOREIGN PATENT DOCUMENTS**

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION
	B1	5632732	04/2/81	Japan	437	240	Abstract (Yes) Patent (No)

**OTHER ART**

(Author, Title, Journal, Volume, Pertinent Pages, & Date)		
	C1	Wolfe et al., Vol. 1, Silicon Processing for the VLSI Era, Lattice Press, 1986, pp0181-191.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*Information Disclosure Statement--PTO-1449 (Modified)*